

INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 970607C	Serial No. Unassigned
	Applicant(s): Shinichiroh IKEMASU et al.	
	Filing Date: April 20, 2004	Group Art Unit: 2814 (expected)

U.S. PATENT DOCUMENTS






Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<i>JA</i>	AA	5,731,130	Tseng	3/1998	430	316	
	AB	5,364,813	Koh	11/1994	438	397	
	AC	5,150,276	Gonzales	9/22/92			
	AD	5,196,910	Moriuchi et al.	3/23/93			
	AE	5,874,756	Ema et al.	2/23/99			
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		Document No.	Date	Country	Translation (Yes or No)		
<i>JA</i>	AO	61-176148	8/7/96	Japan			
	AP	3-167874	7/19/91	Japan			
	AQ	5-218332	8/27/93	Japan			
	AR	JP 6-97190	4/8/94	Japan			
	AS	JP 9-36325	2/7/97	Japan			
	AT	58-115859	7/9/83	Japan			
<i>JA</i>	AU	3-155663	7/3/91	Japan			
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OTHER DOCUMENTS

 	AV	Lee, K.P., et al., "A Process Technology for 1 Giga-Bit DRAM:, 10-13 Dec. 1995, IEDM 95, pp. 907-910			
	AW	Tomihisa MIZUNO et al.; IEEE Transactions on Electron Devices, Vol. 38, No. 3, pp. 584-591; March 1991			
<table border="0" style="width: 100%;"> <tr> <td style="width: 30%;">Examiner </td> <td style="width: 30%;">Date Considered</td> <td style="width: 40%; text-align: center;">8/31/05</td> </tr> </table>			Examiner 	Date Considered	8/31/05
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